

## LS-DYNA® And Intel® Cluster Ready Training Seminars

Faster, More Efficient Deployment of LS-DYNA/MPP on Intel Based Clusters



When you're purchasing your next cluster, LSTC suggests you look to an Intel® Cluster Ready certified cluster for faster, more efficient deployment of LS-DYNA/MPP.

Intel Corporation recently launched, with LSTC's support, Intel® Cluster Ready ([www.intel.com/go/cluster](http://www.intel.com/go/cluster)), a program and technology that helps simplify the deployment, usage and management of clustered computer systems by providing a standardized and replicable way to build clusters running high performance computing applications like LS-DYNA/MPP.

John Hallquist, CEO of LSTC, states "LSTC is confident that...[the] Intel® Cluster Ready program will be welcomed by LS-DYNA worldwide users. Our MPP customers and their IT support staffs will no longer be required to expend resources bringing their clusters online. Set-up and configuration time will be significantly decreased or eliminated entirely as a result of Intel's certification solution. "

The fact that LS-DYNA/MPP has been tested to run on clusters certified as Intel® Cluster Ready means you no longer have to worry "if" LS-DYNA/MPP will run on an Intel® Cluster Ready cluster; instead, you can focus on the features and performance of LS-DYNA/MPP you need to get your job done. Intel® Cluster Ready enables this unique value to LSTC's customers for a wide range of cluster configurations, ranging from 4 to 64 nodes, from any of the system suppliers listed on the Intel® Cluster Ready web page (<http://softwarecommunity.intel.com/articles/eng/1314.htm>).

To learn more about Intel® Cluster Ready you can go to

<http://softwarecommunity.intel.com/articles/eng/1296.htm>

or request a joint LSTC and Intel training session by contacting Marsha Victory [marsha@lstc.com](mailto:marsha@lstc.com) at 925 449 2500.

### LSTC Intel® Cluster Ready Training Seminars

LSTC is currently establishing seminar schedules for Intel® Cluster Ready training seminars at LSTC offices in California and/or Michigan. The seminars

will demonstrate how users can maximize the benefits offered by the Intel® Cluster Ready program and FAQ on LS-DYNA installation.

If you have seminar suggestions, are interested in attending, or have any questions, feel free to send them to Marsha Victory – [marsha@lstc.com](mailto:marsha@lstc.com)

During these sessions, attendees will discover how the Intel® Cluster Ready program and technology package makes it easier to design, build, sell, program, acquire, and deploy clusters built with Intel components

### **About Intel® Cluster Ready**

Key elements of the Intel® Cluster Ready program include:

- Specification— The Intel® Cluster Ready Specification ensures that components in a cluster certified as Intel® Cluster Ready conform to industry standards, or, when no appropriate standards exist, implement best-of-class practices. The specification eliminates sources of variability that do not impact performance, usability, or utility.
- Certification— Every ISV application to be registered as Intel® Cluster Ready must be tested on an Intel® Cluster Ready certified cluster with realistic workloads. Of particular importance to you, ISV's like LSTC must describe any software dependencies beyond those required by the Intel® Cluster Ready Specification; this ensures the smooth installation and proper execution of LS-DYNA/MPP on any certified cluster.
- Tools— The Intel® Cluster Checker, a extensible tool provided free-of-charge on all clusters certified as Intel® Cluster Ready, checks the configuration and performance of the cluster. Cluster Checker provides a full range of tests, analyzing important aspects of cluster organization, functionality, and performance, ensuring consistent cluster-wide operation. When problems are found, the Intel® Cluster Checker provides detailed per-component diagnostic information in addition to the overall pass-fail status. This reporting greatly simplifies problem resolution, allowing you to quickly address the problem's source.